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NOTES/COMMENTS:

Docket #	UNTYP020	By:	MEM	Faxing Date	May 24, 2005
Ser. No:	10/634,636	Filing Date:	8/4/2003	Inv(s)	Rinerson et al.
Title:	A 2-Terminal Trapped Charge Memory Device with Voltage Switchable Multi-Level Resistance				

The following is being transmitted to the U.S. Patent Office:

Item	Description	# Pgs
1.	Petition For Revival of an Application For Patent Abandoned Unintentionally Under 37 CFR 1.137(b)	2
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3.	Responses	12
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